

## Zagdu Singh Charitable 'Trust's (Regd.) THAKUR COLLEGE OF

A - Block, Thakur Educational Campus, Shyamnarayan Thakur Marg, Thakur Village, Kandivali (East), Mumbai - 400 101.



RANGUE COLLEGE OF

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(Approved by AICTE, Govt. of Maharashtra & Affiliated to University of Mumbai\*)

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(Accredited Programmes by National Board of Accreditation, New Delhi\*\*)

\*Permanent Affiliated UG Programmes: "Computer Engineering • Electronics & Telecommunication Engineering • Information Technology (we.£:AY.2015-16 onwards)

\*\*21st time Accredited UG Programmes: "Computer Engineering • Electronics & Telecommunication Engineering • Information Technology • Electronics Engineering • Information Technology • Electronics Engineering (3 years w.e.f.:01-07-2016)

TCET/FRM/IP-02/10 Revision: B

### **Semester Plan** (Practical / Tutorials / Assignment)

Course: T.E. EXTC Semester: VI Batches: T.E (A1A2A3A4)

Subject: Mini project II Class: T.E EXTC- A Batch size: 20 Students

Lab. Assistant / Attendant: Mr. Dinesh Laboratory faculty in charge: Ms. Purnima C.

(Lab Attendant 102)

Note: Experiment planned as per University Curriculum

#### **Basic Experiments:**

Sr. No.	TITLES Experiments / Tutorials / Assignment (Planning with use of Technology)	Planned Dat A1 - A2- A3-			-	Completion Date	Remarks
1.	Literature Survey on projects domain	27/ 7	27/ 7	26/ 7	26/ 7		
2.	To prepare synopsis and presentation of selected project	3/8	3/8	2/8	2/8		
3.	To present project idea for selected project title	10/ 8	10/ 8	9/8	9/8		
4.	Implementation of circuit on breadboard /study of software for code writing /learning the simulation	24/ 8	24/ 8	16/ 8	16/ 8		

#### **Design/ Development Experiments:**

5.	Testing of circuit / code writing and testing	31/ 8	31/ 8	30/ 8	30/ 8	
6.	Progress presentation	7/9	7/9	6/9	6/9	

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ENGINEERING & TECHNOLOGY

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\*\*2nd time Accredited UG Programmes: \*Computer Engineering \*Electronics & Telecommunication Engineering \*Information Technology

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\*\*Information Technology \*Electronics Engineering (3 years w.e.f::01-07-2016)

7.	PCB implementation/ Simulation results					14/ 9	13/ 9	13/ 9			
8.	PCB implementation/ Simulation results					21/ 9	20/ 9	20/ 9			
Group	Group Learning Activity:										
9	Demo pro	5/1 0	5/1 0	4/1 0	4/1 0						
10	Submission of final report and prototype					12/ 10	13/ 10	13/ 10			
	Bridge courses Objective: Bridging of gaps with respect to prerequisites and industry skills or to carryout research in that particular field. (30 Hrs / Semester / student)										
The ar	Mini /Minor Projects Objective: To get hands on experience to execute projects with respect to student choice in the following areas. (30 Hrs / Semester / Student). (Total 120 Hrs)  The areas are:     1. Research 2. Core 3. Interdisciplinary 4. Application  Major project: As per University Scheme										
S.No	Project Title/Group Size			Class	Type / Project Hours			t	Modes of Learning		Referen ce
1.	Finger print and pattern with code security vault  (3 - 4 Students)			T.E EXTC	Application / 120 Hrs			20	Project Based Learning		
2.	Hand assistive system for Deaf, Dumb and blind people (3 - 4 Students)			T.E EXTC		Application / 120 Hrs			Project Based Learning		
No. of	Planned Completed No. of Presen		Planned	Completed				N o.	Planned	Complete d	
Prac				02					of	02	

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Date: 10/07/2017

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	<b>Exp:</b> 04					R		
	Design					е		
	Base					р		
	<b>Exp</b> : 04					0		
						rt		
	Group					S		
	Learnin							
	<b>g:</b> 02							
	Major							
	Project:							
	02							
DOSLNE:			DOSLE (engaged in some other dates):					

Group activities are required to be added with the practical related to course to enhance the learning activity of the student in the course. Group activity includes: Group presentation, new experiment design, mini projects etc.

- 1. The practical plan date and completion date shall be in compliance. For any non-compliance reason(s) required to be stated in remark column.
- 2. Learning objective and outcome shall be clearly stated with each of experiments/ tutorials/ assignments and are required to be mapped at the end of the semester.
- 3. Entry for DOSLE (engaged on some other date) shall be done with proper mapping to DOSLNE.

3D-	30-
Signature of HOD	Signature of Principal / Dean Academic

Date: 10/07/2017

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